

POSITIONING OF INVERTING BUFFERS IN A NETLIST

ABSTRACT

A method, apparatus, system, and signal-bearing medium that, in an embodiment, position inverting buffers to improve placement of a logic circuit. An inverting buffer,
5 within a netlist, is moved from a source region to a sink region, where the source region and the sink region are connected via inverting and non-inverting routes. Moving the inverting buffer eliminates one of the routes from the source region to the sink region. In this way, routing congestion and total wire length may be reduced.